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task switching <near> deferring <near> blocked   [Search]

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Cache Memories - all 18 versions »
AJ Smith - ACM Computing Surveys (CSUR), 1982 - portal.acm.org
... **near** future are likely to be **near** the current ... sometimes referred to as a **block**.)
Replacement Algorithm ... The frequent **switching** between user and supervisor state ...
Cited by 1232 - Related Articles - Web Search

The attentional blink: Resource depletion or temporary loss of control? - all 5 versions »
V Di Lollo, J Kawahara, SM Shahab Ghorashi, JT ... - Psychological Research, 2005 - Springer
... Inserted **near** the end of the distractor stream was ... to the phenomenon known as residual
switch cost in ... signals for this initial monitoring **task** are discontinued ...
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[PDF] The attentional blink: Resource depletion or temporary loss of control?
SMS Ghorashi, JT Enns - Psychological Research, 2005 - psych.ubc.ca
... Inserted **near** the end of the distractor stream was ... to the phenomenon known as residual
switch cost in ... signals for this initial monitoring **task** are discontinued ...
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[PDF] Episode 9: Sensor Networks - all 3 versions »
H Frey, P Sturm - uni-trier.de
... nodes – Multiprocessing, preemptive **task switching**, virtual memory ... value pairs
describing sensing **task** • Interest Propagated ... to the same device **near** to the ...
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[PDF] Episode Episode 9: 9: Sensor Networks Sensor Networks - all 2 versions »
O Outline - syssoft.uni-trier.de
... nodes – Multiprocessing, preemptive **task switching**, virtual memory ... value pairs
describing sensing **task** • Interest Propagated ... to the same device **near** to the ...
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Integrated overload control for overload control for distributed real time systems - all 4 versions »
GM Gehi, SL Lin, KA Sohraby - US Patent 6,134,216, 2000 - freepatentsonline.com
... permits the system to respond in a **near** optimum fashion ... may also include **deferring**
a critical **task** performed by ... to the **switch** and relieve the **switch** nodes of ...
Cited by 4 - Related Articles - Cached - Web Search

Integrated overload control for distributed real time systems - all 4 versions »
GM Gehi, KA Sohraby, SL Lin - EP Patent 0,913,772, 2007 - freepatentsonline.com
... permits the system to respond in a **near** optimum fashion ... may also include **deferring**
a critical **task** performed by ... to the **switch** and relieve the **switch** nodes of ...
Related Articles - Cached - Web Search

[PDF] Lazy Queueing and Direct Process Switch—Merit or Myths? - all 4 versions »
K Elphinstone, D Greenaway, S Ruocco - Workshop on Operating Systems Platforms for Embedded Real- ... - ertos.nicta.com.au
... being suffi- cient to support **near**-monolithic system ... Benchmark **Task** brk_test Carry
out the brk syscall in a loop. ... we see that direct process **switching** and lazy ...
Cited by 1 - Related Articles - View as HTML - Web Search

[PDF] Theorizing the Unintended Consequences of Instant Messaging for Worker Productivity - all 10 versions »
J Rennecker, L Godwin - Sprouts: Working Papers on Information Environments, Systems ..., 2003 - interruptions.net
... tool which allows for **near**-synchronous computer ... individual productivity mediated
by **task** interruptions. ... language, and tone, when **switching** between conversations ...
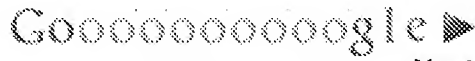Cited by 13 - Related Articles - View as HTML - Web Search

Apparatus, system and method for control of speed of operation and power consumption of a memory - all 4 versions »
BS Haroun, U Ko - US Patent 6,151,262, 2000 - freepatentsonline.com
... The invention proposes **deferring** return to a lower power **...** Such compound decisions
would enable **near** optimal powering **...** could thus be loaded upon each **task switch**. **...**
Cited by 3 - Related Articles - Cached - Web Search

Key authors: **A Smith** - **B Randell** - **R Love** - **L Williams** - **R Kessler**

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[PDF] Reconstructing I - all 7 versions »
K Fraser, S Hand, R Neugebauer, I Pratt, A ... - leitl.org
... the port-access bitmap when **context- switching** between domains ... Xen) and allows the
necessary address-space **switch** if a ... in commodity chipsets in the **near** fu- ture ...
Cited by 24 - Related Articles - View as HTML - Web Search

[PDF] Technical Report
I Pratt, A Warfield, M Williamson - cl.cam.ac.uk
... the port-access bitmap when **context- switching** between domains ... Xen) and allows the
necessary address-space **switch** if a ... in commodity chipsets in the **near** fu- ture ...
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A User-Level Approach To Network Attached Storage - all 3 versions »
R Sohan, S Hand - Proceedings of the The IEEE Conference on Local Computer ..., 2005 - doi.ieeecomputersociety.org
... in the **context** of networking by **deferring** packet processing ... kernel, at which time
a **context switch** will have ... quickly achieving and maintaining **near**-link speed. ...
Related Articles - Web Search - BL Direct

System support for automatic profiling and optimization - all 7 versions »
X Zhang, Z Wang, N Gloy, JB Chen, MD Smith - ACM SIGOPS Operating Systems Review, 1997 - portal.acm.org
... also modified the kernel to log **context switch** information so ... to make it meaningful
in the **context** of the ... the algorithm attempts to place them **near** each other ...
Cited by 148 - Related Articles - Web Search - BL Direct

The measured performance of personal computer operating systems - all 32 versions »
JB Chen, Y Endo, K Chan, D Mazières, A Dias, M ... - ACM Transactions on Computer Systems (TOCS), 1996 - portal.acm.org
... initiate **context switches** manually. ... space. **Context switches** between applications
occur when an application voluntarily yields control. ...
Cited by 95 - Related Articles - Web Search - BL Direct

Application performance in the QLinux multimedia operating system - all 12 versions »
V Sundaram, A Chandra, P Goyal, P Shenoy, J Sahni, ... - Proceedings of the eighth ACM international conference on ..., 2000 - portal.acm.org
... crating systems in the **near** future. ... This is achieved by **deferring** protocol processing

from ... operating system performance such as **context switching**, memory, file ...
Cited by 63 - Related Articles - Web Search

Dynamic Instrumentation of Production Systems - all 18 versions »
BM Cantrill, MW Shapiro, AH Leventhal - usenix.org
... for speculatively tracing data, **deferring** the decision ... by carefully placing SDT probes
near extant calls ... by 64 percent, involuntary **context switches** dropped by ...
Cited by 95 - Related Articles - Web Search

TightLip: Keeping applications from spilling the beans - all 6 versions »
AR Yumerefendi, B Mickle, LP Cox - Proc. 2007 NSDI, 2007 - usenix.org
... the likelihood of divergence by **deferring** signal delivery ... a complete environment
in the very **near** future. ... began to dominate the **context switch** overhead induced ...
Cited by 5 - Related Articles - Cached - Web Search

[PDF] A Distributed Object Platform Infrastructure for Multimedia Applications - all 5 versions »
G Coulson, MW Clarke - Computer Communications, 1998 - comp.lancs.ac.uk
... Minimisation of **context switches** can be achieved through the use ... handler simply sets
the event flag, **deferring** to the ... is TRUE, the call will **block** until space ...
Cited by 43 - Related Articles - View as HTML - Web Search

Platypus: Design and implementation of a flexible high performance object store - all 13 versions »
Z He, SM Blackburn, L Kirby, JN Zigman - Proceedings of the Ninth International Workshop on Persistent ..., 2000 - Springer
... have not strongly targeted this **domain**, so strong ... flag **blocked** on condition variable
[sleep] [**block** on condition ... 3. Overlapping I/O with **context switches** to a ...
Cited by 7 - Related Articles - Web Search - BL Direct

Key authors: J Chen - M Smith - X Zhang - K Fraser - V Sundaram

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[CITATION] Impact of Behavior Influence on Decentralized Control Strategies for Swarms of Simple, Autonomous, ...
DW Palmer, CM Hantak, MA Kovacina - Biomechanics meets Robotics Modelling and Simulation of ...
Cited by 5 - Related Articles - Web Search

TightLip: Keeping applications from spilling the beans - all 6 versions »
AR Yumerefendi, B Mickle, LP Cox - Proc. 2007 NSDI, 2007 - usenix.org
... the likelihood of divergence by **deferring** signal delivery ... any pointers to the original
process's **task** structure ... this behavior was that during **swapping**, the sshd ...
Cited by 5 - Related Articles - Cached - Web Search

TOSSIM: accurate and scalable simulation of entire tinyOS applications - all 39 versions »
P Levis, N Lee, M Welsh, D Culler - Proceedings of the 1st international conference on Embedded ..., 2003 - portal.acm.org
... to be responsive, returning immediately while **deferring** extensive computation to ...
The **task** signals the re- ceive event on ... event to perform a buffer **swap** for its ...
Cited by 522 - Related Articles - Web Search

An introduction to parallel rendering - all 8 versions »
TW Crockett - Parallel Computing, 1997 - Elsevier
... parallelism may be obtained by decomposing the problem in the time **domain**. ... **deferring**
task partitioning decisions until one or more processors becomes idle, at ...
Cited by 46 - Related Articles - Web Search

SECTION 1.1 Adaptive System-Level Diagnosis in Real-Time1
ME Stahl, RP Bianchini Jr - Foundations of Dependable Computing: Paradigms for ..., 1994 - books.google.com
... If a **task** relinquishes the processor, **deferring** a portion of its execution, it can ...
other N- 1 **tasks** of equal priority are assumed to **block** the given **task**. ...
Web Search

Adaptive System-Level Diagnosis in Real-Time^
ME Stahl, RP Bianchini Jr - Springer

... If a **task** relinquishes the processor, **deferring** a portion of its execution ... protocol
(PCP) limits the time a higher priority **task** can be **blocked** waiting for ...
Related Articles - Web Search

[PDF] Concrete architecture of the linux kernel - all 9 versions »
I Bowman, S Siddiqi, M Tanuan - URL: http://plg. uwaterloo. ca/itbowman/CS746G/a2, 1998 - kmh.yeungnam-c.ac.kr
... Access (**swap**) - this group of modules controls memory **swapping**. ... allows the CPU to
process other **tasks** while the ... By **deferring** non-critical work to a bottom half ...
Cited by 18 - Related Articles - View as HTML - Web Search

[PS] Formally Defining Coordination Processes to Support Contract Negotiation - all 6 versions »
AGCHL Barbara, SLLJ Osterweil - cs.umass.edu
... is used simply to allow the necessary **swapping** on the ... from the offerer who desires
a **task** to be ... as it is received instead of **deferring** evaluation until the ...
Related Articles - View as HTML - Web Search

TinyOS: An Operating System for Sensor Networks - all 9 versions »
P Levis, S Madden, J Polastre, R Szewczyk, K ... - Ambient Intelligence, 2005 - Springer
... events cannot **block**: rather, a request for service ... return- ing immediately while
deferring extensive computation ... system components along with a **task** scheduler. ...
Cited by 76 - Related Articles - Web Search

Restructuring Computations for Temporal Data Cache Locality - all 10 versions »
VK Pingali, SA McKee, WC Hsieh, JB Carter - International Journal of Parallel Programming, 2003 - Springer
... CUDD 2×hash-table – one variable **swap** size EM3D (4×degree +12) 40K ... 1, **deferring** queries,
inserts, and deletes requires changes to both control and data ...
Cited by 7 - Related Articles - Web Search - BL Direct

Key authors: **P Levis** - **M Kaashoek** - **M Welsh** - **D Culler** - **J Jannotti**

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[PDF] A library implementation of POSIX threads under **UNIX** - all 18 versions »
F Mueller - Proceedings of the USENIX Conference, 1993 - www-md.e-technik.uni-rostock.de
... Therefore, a compare-and-**swap** instruc- tion should be provided in the ... an Ada runtime
system which is able to map Ada **tasks** onto threads ... **UNIX signal** handler 154 ...
Cited by 162 - Related Articles - View as HTML - Web Search

Task swap out in a multithreaded environment
GA Alverson, II Callahan, C David, SL Coatney, BD ... - US Patent 7,360,221, 2008 - freepatentsonline.com
... the **swap** in. The **task** master stream creates a stream to process any **Unix signals**,
and all the other team master streams wait until the **Unix signal** processing ...
Cached - Web Search

User program and operating system interface in a multithreaded environment
GA Alverson, CD Callahan, C Charles David, L Susan ... - US Patent 6,952,827, 2005 - freepatentsonline.com
... 512, the routine sets the **signal** — wait$ synchronization variable of the **task**
swap header to ... the processing of the **Unix signals** is complete. ...
Related Articles - Cached - Web Search

[PS] UTOPIA: Load Sharing in Large-Scale Heterogeneous Distributed Systems - all 5 versions »
S Zhou... - csri.toronto.edu
... Non-Ultrix host with at least 12 MB mem, 25 MB **swap**, and ... Maintain identical **Unix**
execution environment ... Support terminal I/O using pty between RES & remote **task** ...
Cited by 2 - Related Articles - View as HTML - Web Search

Stream management in a multithreaded environment - all 3 versions »
GA Alverson, CD Callahan, SL Coatney, BD Koblenz, ... - US Patent 7,191,444, 2007 - Google Patents
... in **Task** Swi p Header 503 task_master = tera_team_swapsave complete (numoff streams
ofteam) task_master = Fetch and add 1 to **signal** wait ofTask **Swap** Header ...
Related Articles - Web Search

[PDF] SCE: A Fully Integrated Software Tool for Beowulf Cluster System - all 10 versions »
P Uthayopas, S Phatanapherom, T Angskun, S ... - HPC Revolution - democritos.it
... disk is used for data storage and **swap** space only. ... KSIX subsystem that responsible
for this **task** are called ensemble ... will be added in SCE in the **near** feature. ...
Cited by 15 - Related Articles - View as HTML - Web Search

[PDF] SiGe HBT BiCMOS Field Programmable Gate Arrays for Agile Mixed **Signal** Applications
JF McDonald, R Kraft, K Zhou, JR Guo, Y Chao, R ... - eecs.usma.edu
... 12181 1-518-276-2919 / mcdonald@**unix**.cie.rpi ... at the top of the circuit **near** the output ...
being shifted in, creating nearly seamless **task swapping** environment, and ...
Related Articles - View as HTML - Web Search

MBCF: a protected and virtualized high-speed user-level memory-based communication facility - all 5 versions »
T Matsumoto, K Hiraki - Proceedings of the 12th international conference on ..., 1998 - portal.acm.org
... The target **task** can check synchronizations with only direct ... calls of the conventional
OSs like **UNIX** have the ... or system-level maintenances (eg **signal** check or ...
Cited by 18 - Related Articles - Web Search

System and method for providing cooperative interrupts in a preemptive **task** scheduling environment - all 8 versions »

RT Williams, KB Jacklin, JL Robbin, JJ Iarocci - US Patent 5,911,065, 1999 - freepatentsonline.com

... executes each interrupt handler via a **Unix** "**signal**" operation directed ... Atomic operations,
eg, a compare and **swap** operation, are ... the kernel 218 and a **task** or an ...

Cited by 3 - Related Articles - Cached - Web Search

Implementing real-time robotic systems using CHIMERA II - all 6 versions »

DB Stewart, DE Schmitz, PK Khosla - Systems Engineering, 1990., IEEE International Conference on, 1990 - ieeexplore.ieee.org

... The total address space used by all **tasks** on one ... memory, as the memory man- agement
and **swapping** ovehead not ... The CHIMERA I1 kernel uses a **UNIX**-like approach of ...

Cited by 36 - Related Articles - Web Search

Key authors: **F Mueller** - **F Delaglio** - **D Stewart** - **S Grzesiek** - **G Vuister**

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deferred task resume and multitasking   Search

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User controllable concurrent functionality. - all 3 versions »
RE Jennings Jr, BP Haley Jr, RD Holland, DK Cecil, ... - EP Patent 0,685,791, 1995 - freepatentsonline.com
... In these cases, this type of **task** can be demoted to an ordinary **deferred** write **task**
(with all ... Execution of the **task** stops and waits for a **resume** or abort ...
Cached - Web Search

Method and apparatus for multi-tasking independently executable programs - all 3 versions »
WC Russell, RD Wadsworth - EP Patent 0,598,508, 2003 - freepatentsonline.com
... a non-relinquishing **task**, and to **resume** execution of ... is a non-preemptive **multitasking**
monitor which ... requires that each application **task** periodically relinquish ...
Related Articles - Cached - Web Search

[CITATION] RT-MINIXv2: Architecture and Interrupt Handling
FR Santa Fe
Related Articles - Web Search

Microprocessor inverse processor state usage - all 3 versions »
US Patent 5,161,226, 1992 - freepatentsonline.com
... 9" of the register 35 defines a **resume** field 41 ... interrupt is posted and interrupt
servicing is **deferred**, step 85. ... is of higher priority, the user **task** step 77 ...
Cited by 28 - Related Articles - Cached - Web Search

[PDF] Recovering context after interruption - all 11 versions »
JL Franke, JJ Daniels, DC McFarlane - Proceedings 24th Annual Meeting of the Cognitive Science ..., 2002 - ail.imco.com
... terrupted activities when they try to **resume** them. ... If the interruption is **deferred**,
the interruption server ... rather than about the interrupting **task** itself), we ...
Cited by 18 - Related Articles - View as HTML - Web Search

[PDF] COCKPIT INTERRUPTIONS AND DISTRACTIONS: A LINE OBSERVATION STUDY Dr. Loukia D. Loukopoulos US Navy/ ... - all 9 versions »
RK Dismukes, I Barshi - humansystems.arc.nasa.gov
... is a less well understood aspect of **multi- tasking**. ... intentions and remember to **resume**
suspended activities ... Challenges of Managing Concurrent and **Deferred Tasks**. ...
Related Articles - View as HTML - Web Search

Engineering and analysis of fixed priority schedulers - all 8 versions »
DI Katcher, H Arakawa, JK Strosnider - Software Engineering, IEEE Transactions on, 1993 - ieeexplore.ieee.org
... that guarantee individual **task** execution times in **multitasking** interrupt driven ... T3
is already running, the interrupt for **task** r will be **deferred** until Ti ...
Cited by 158 - Related Articles - Web Search - BL Direct

[PDF] Scheduling in RealTime Operating Systems
R Gupta - ics.uci.edu
... requests of a lower priority are ignored or **deferred** and requests of ... at its point
of interruption and begin or **resume** execution of the higher priority **task**. ...
View as HTML - Web Search

Assisting operating-system interrupts using application-based processing

AH Simon - US Patent 5,778,207, 1998 - freepatentsonline.com

... and perform some other needed **task** or run **...** running immediately before receiving the
deferred interrupt (step 35 **...** O and microprocessor M2 will **resume** execution of **...**

Cited by 1 - Related Articles - Cached - Web Search

[PDF] Seminar Paper

M Luft - rig.informatik.tu-chemnitz.de

... a lot of advanced features like interrupt handling, real time **task** switching and **...**
With **deferred** coalescing it is not sure, whether the previously freed blocks **...**

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Key authors: **M Freed** - **D Kieras** - **D Katcher** - **J Ballas** - **E Lauber**

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